Software Validation Engineer*

High competence and the passion for new challenges make our Electrics/Electronics division the guidepost to the future. Take your next step and support our team!

Activities:

- Requirements analysis
- Writing and execution of tests for the verification of the system and sw functions
- Documentation of results
- Management of anomalies

Basic requirements for holding the position:

- Degree in Electronic or Computer Engineering or Telecommunications or similar
- Experience 5 years in software verification and validation in real-time/embedded contexts
- Knowledge of black-box testing methodologies
- Knowledge of V-model like ASPICE (or CMMI) processes
- Knowledge of bug-tracking management tool (Redmine/JIRA)
- Knowledge of requirement management tool (DOORS)
- Knowledge of Italian and English languages

Preferential requirements:

- Experience in automotive, rail or bio-medical sectors
- Knowledge of tools for static and dynamic code analysis
- CAN/LIN/UDS/J1939 bus knowledge
- Knowledge of the Functional Safety (ISO 26262 / 25119)
- ISTQB certification

Location: Reggio Emilia (RE)

* Gender, age, nationality, or religion are not relevant for us – you are all that matters.

Sounds like your next challenge? Then we'll be looking forward to your application!

For all information about EDAG as an employer and further career opportunities, please visit edag.com/career.

MAKING IMPOSSIBLE
THINGS POSSIBLE.
WITH YOU.

Software Validation Engineer*

Additional information

Location	**Reggio Emilia**
Position type	**Full-time employee**
Start of work	**Apr 21, 2025**

Responsible

Serena Nicoli Marchesini